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**** CONTINUING DATA *******

This application is a CON of 10/075,246 02/15/2002 PAT 6,664,642 which is a CON of 09/846,260 05/02/2001 PAT 6,433,438
 which is a DIV of 09/050,416 03/31/1998 PAT 6,261,883

**** FOREIGN APPLICATIONS *******

JAPAN 9-81013 05/31/1997
 JAPAN 10-33388 02/16/1998

IF REQUIRED, FOREIGN FILING LICENSE GRANTED **

10/10/2003

Foreign Priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 27	TOTAL CLAIMS 6	INDEPENDENT CLAIMS 4
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged Examiner's Signature _____ Initials _____				

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TITLE

SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

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